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Digital Design Homework # 1 (Chapter 1) INSTRUCTIONS: o Show your work (i.e., how you derived your answer or the reason behind your thinking) in addition to your answer. o Homework and solution must be printed and solved on the same paper. o Deadline : Wednesday, 19/9/2012 (not late homework will be accepted)

Digital Design Homework # 1 (Chapter 1)

Homework. Stay updated via RSS. Categories. AI (7) Chapter 1 (1) Chapter 2 (1) Chapter 3 (1) Chapter 4 (1) Chapter 5 (1) Chapter 6 (1) ... 2013 in Chapter 7, Digital Design. 0. Sequential Circuits. Sequential circuits are circuits the have some type of memory to remember the past input values, they use latches and flip-flops.

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6: Design with Multiplexers 453 9.8 Experiment 7: Adders and Subtractors 455 9.9 Experiment 8: Flip-
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an essential element of electronics and computer science, and their significance is increased regularly.
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Each chapter is a combination of theory followed by review exercises to be completed as traditional
homework assignments. Full solutions to all of the review exercises are available in the last appendix.
Procedures for labs then follow that allow the student to implement the concepts in a hands on manner.
The materials required for the labs

Introduction to Digital Logic with Laboratory Exercises

Tools you'll need: Aldec Active-HDL simulator Download site used for simulating the Verilog code, the
website to download a free student version of the Aldec Active-HDL simulator.. ISE WebPACK
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