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fabricated and tested in a 40nm CMOS technology. For this kind of circuit, there is an obvious trade-off between output frequency and power consumption and in order to increase the first, the ...

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Instead of carving a, say, new processor from a single slab of silicon, chipmakers assemble it from different smaller pieces, which are then connected (see inset "What are chiplets?"). You won't have ...

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Chain up the next carry out to a third 4017, and you can count up to 1,000 ... these voltage steps play well with our quick-and-dirty CMOS logic synth modules. The sequencer

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and compact design is a must for CMOS-realizable photonic integrated circuits. Third, the relatively high optical isolation ratio implies the capability to support large-scale photonic integrated ...

~~A chip scale one way valve for light~~

Geada: The first transistor was in the late 1960s, and the first integrated circuits from Intel were in 1970 ... Sure, you can integrate some of it with normal conventional CMOS, but it's easier when ...

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Practically every electronic design attempted today involves ... the low-pass filter effects of the decimation circuit will lower second- and third-order harmonics. Decimation also improves ...

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Conversely, an op-amp circuit can synthesize a negative resistance ... tunnel structures have struggled to catch up with dominant CMOS processes. Since the original Esaki diode, various materials ...

~~Use Nonlinear Devices As Linchpins To Next Generation Design~~

The sub-millimeter cube, which measures

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temperature, merges CMOS and piezo for power ... How a piezoelectric sensor and oscillator circuit can be used to sense and report on temperature.

~~Skip the Implantables This Tiny Sensor/Telemetry "Mote" is Syringe-Injectable~~
The citation data (more than 800 in Web of Science, and 1,400 in Google Scholar) make this the third most-cited paper in the IEE ... to be valuable beyond the original scope of bipolar circuit design ...

~~IET Journals: the papers that paved the way --(BUSINESS WIRE)--Phison Electronics Corp. (TPEX: 8299), a global leader in NAND flash controller integrated circuits and ... charge traps with a CMOS-under-array (CuA) design.~~

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