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~~Transistors:~~

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_____ ? What is

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Types - Voltage
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circuit MOSFETs

explained ~~MOSFET~~

~~Basics || n \u0026amp;~~

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~~MOSFETs~~ ||

~~MOSFET Drivers~~

Building logic gates
from MOSFET
transistors

The Fabrication of
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Bios)
(CMOS)

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COST OF DELAY
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ctor, also known as
complementary -
symmetry metal – ox
ide – semiconductor,
is a type of metal – o
xide – semiconductor
field-effect
transistor
fabrication process
that uses
complementary and
symmetrical pairs
of p-type and n-

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type MOSFETs for logic functions. CMOS technology is used for constructing integrated circuit chips, including microprocessors, microcontrollers, memory chips, and other digital logic circuits. CMOS technology is also used for analog ...

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Billions 3 Billion 6

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47 2. 1.2 1.1 3. 1.3

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Ø Largest fraction of digital IC market until 80 ' s. Ø

However, power consumption per gate sets upper limit on the integration density.

- MOS (Metal-Oxide-Silicon) Ø

First CMOS

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introduced in 1963.

Ø PMOS-only used through early 70 ' s

(in the first

practical MOS

integrated circuits,

e.g., calculators).

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